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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	10/044/365
Filing Date	January 11, 2002
First Named Inventor	Xiaobao Wang, et al.
Art Unit	2811
Examiner Name	
Attorney Docket Number	015114-054910US

1 of 2

U.S. PATENT DOCUMENTS

Examiner	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
AC	1	5,164,663		11-17-1992	Alcorn	
	2	5,359,235		10-25-1994	Coyle et al.	
	3	5,374,861		12-20-1994	Kubista	
	4	5,726,583		03-10-1998	Kaplinsky	
	5	5,764,080		06-09-1998	Huang et al.	
	6	5,864,715		01-26-1999	Zani et al.	
	7	5,955,911		09-21-1999	Drost et al.	
	8	5,970,255		10-19-1999	Tran et al.	
	9	6,008,665		12-28-1999	Kalb et al.	
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	11	6,037,798		03-14-2000	Hedberg	
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	13	6,064,224		05-16-2000	Esch, Jr. et al.	
	14	6,087,847		07-11-2000	Mooney et al.	
	15	6,100,713		08-08-2000	Kalb et al.	
	16	6,157,206		12-05-2000	Taylor et al.	
	17	6,236,231 B1		05-22-2001	Nguyen et al.	
	18	6,252,419 B1		06-26-2001	Sung et al.	
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	20	6,362,644 B1		03-26-2002	Jeffery et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				

Examiner
Signature

Paul D. Gray

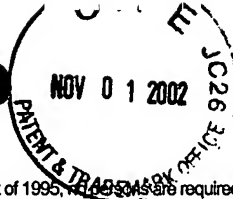
Date
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3/21/03

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PTO/SB/08B (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

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**INFORMATION DISCLOSURE
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Sheet 2 of 2

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Application Number	10/044/365
Filing Date	January 11, 2002
First Named Inventor	Xiaobao Wang, et al.
Art Unit	2811
Examiner Name	
Attorney Docket Number	015114-054910US

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
GC	21	ALTERA, Apex 20K "Programmable Logic Device Family," Altera Corporation, Ver. 1.1, May 2001.	
↑	22	ALTERA, Apex II "Programmable Logic Device Family," Altera Corporation, Ver. 1.1, May 2001	
↓	23	ESCH and MANLEY, Theory and Design of CMOS HSTL I/O Pads, The Hewlett Packard Journal, August 1998.	
GC	24	XILINX, "Virtex-II 1.5V Field Programmable Gate Arrays," Xilinx, DSO3102 (v1.5), April 2, 2001.	

Examiner Signature	<i>Paul D. Gray</i>	Date Considered	3/21/03
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